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	Filing Date		2004-03-31
	First Named Inventor	Grochowski, Edward T.	
	Art Unit	2183	
	Examiner Name	Fennema, Robert E.	
Attorney Docket Number		P18305	

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2	476915	TW		2002-02-21	Kalafatis et al.	<input checked="" type="checkbox"/>
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9	Office Action Received for Japanese Patent Application No. 2007-506565 mailed on August 31, 2010, 6 Pages of Office Action including English Translation.	<input checked="" type="checkbox"/>
10	HANKINS et al., "Multiple Instruction Stream Processor", Proceedings of the 33rd International Symposium on Computer Architecture (ISCA'06), 2006, pp. 2-17/E.	<input type="checkbox"/>
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14	SASADA et al., "Implementation and Evaluation of a Thread Library for Multithreaded Architecture", Proceedings of the Symposium on Advanced Computing System and Infrastructure (SACIS) 2003, Information Processing Society Japan (IPSJ), 28 May 2003, Vol. 2003, No. 9, pp 1-9/E.	<input type="checkbox"/>

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